## **PATENT APPLICATION**



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT:

Hyung-joon Kwon

**DOCKET NO.** 8021-28 (SS-14984-US)

**SERIAL NO:** 

09/765,539

GROUP:

Art Unit 2133

FILED:

January 19, 2001

**EXAMINER:** Torres, Joseph D.

FOR:

METHOD AND APPARATUS FOR CORRECTING C1/P1 WORD ERRORS USING ERROR LOCATIONS DETECTED BY EFM/EFM+

**DECODING** 

RECEIVED

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

DEC 2 2 2003

Technology Center 2100

## RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Office Action dated November 19, 2003, applicant provisionally elects the claims of Group I (Claim nos. 1-16), with traverse. While the invention of Group I may be distinct for the reasons set forth by the Examiner, applicant believes that simultaneous examination would not present an undue burden. For example, the claims of Group I are generally drawn to Memory Access for Error Correction Coded Data, classified in class 714, subclass 769; while the claims of Group II are generally drawn to Decoding of Error Correction Coded Data, classified in class 714, subclass 774. Under such circumstances, the Examiner is encouraged to maintain all claims in the same application. See, MPEP § 803.

## CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on <u>December 16, 2003</u>.

Dated: Dec. 16, 2003

Eric M. Parhan

All issues raised by the Examiner having been addressed, early and favorable consideration of this application are earnestly solicited.

Respectfully submitted,

By:

Eric M. Parham Reg. No. 45,747

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